

ENERGY EFFICIENCY OF PLANAR DISCHARGE FOR INDUSTRIAL APPLICATIONS

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ABSTRACT. Diffuse Coplanar Surface Barrier Discharge has proven its capabilities as an industry-ready plasma source for fast, in-line and efficient plasma treatment at atmospheric pressure. One parameter required by industry is energy efficiency of the device. In this paper, we present the energy efficiency of the whole plasma system, and we investigate possible sources of errors.

KEYWORDS: dielectric barrier discharge; DCSBD; electrical efficiency; power estimation; error analysis.

1. INTRODUCTION

Diffuse Coplanar Surface Barrier Discharge (DCSBD) is a special type of dielectric barrier discharge [1] that uses a planar parallel arrangement of electrodes [2, 3] embedded in the ceramic plate dielectric barrier and a dielectric cooling oil. DCSBD is a non-isothermal discharge [4, 5] which is able to generate macroscopically homogeneous plasma at atmospheric pressure in air and also in other working gases (O_2 , N_2 , CO_2 , H_2 , H_2O , etc.) [6–8]. The planar geometry and the macroscopically homogeneous plasma generated as a thin plasma layer (approx. 0.3 mm) make DCSBD an appropriate plasma source for treating planar surfaces with roughness below approx. 1 mm. DCSBD has been used with positive results for treating of non-woven textiles [9], wood [10], silicon wafers [11], various foils and fibers [12–14], glass [7], metals [8] and bio-materials [15]. In the work presented here, the energy efficiency of DCSBD was estimated in order to improve the electrical circuit parameters of the DCSBD device, if necessary. The energy efficiency was determined as the ratio of the power-to-plasma estimate and the measured electric power consumption of the electric power supply unit. The power-to-plasma estimates were based on direct power integration from the current and voltage measurements. The energy efficiency was measured in the power range from 100 to 400 W. The oscilloscope parameters and the measurement set up were also investigated as possible sources of errors in power estimates.

2. EXPERIMENTAL PROCEDURE

2.1. DISCHARGE SOURCE

The DCSBD device can be used in various geometric configurations. The dielectric barrier can be a flat or bent plate of 96% alumina ceramic. The electrode system can consist of electrodes of various thicknesses or with various electrode gap widths. For the measurements presented here, the planar DCSBD device has flat dielectrics with electrodes 1.5 mm in thickness and 1 mm spacing between the electrodes. Fig. 1

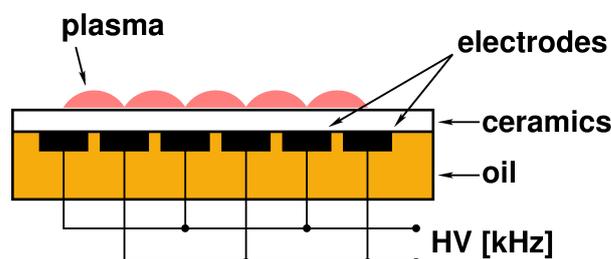


FIGURE 1. Simple scheme of the DCSBD device. Plasma layer, dielectric plate, cooling oil and electrodes on high voltage (HV).

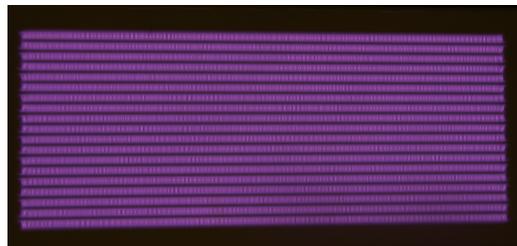


FIGURE 2. Image of DCSBD discharge generated in air at a power input of 400 W. The discharge area is about $8 \times 20 \text{ cm}^2$.

shows the simplified cross-section of the DCSBD device. The periodic structure of high voltage (HV) and grounded electrodes as well as dielectric cooling oil and flat ceramic barrier are depicted.

The DCSBD discharge generated in air at atmospheric pressure and at an input power of 400 W at 15 kHz is given in Fig. 2. The visually homogeneous periodic structure of the discharge across the electrodes can be seen.

2.2. ELECTRIC PARAMETERS MEASUREMENTS AND EXPERIMENTAL SET-UP

For measurements of electrical efficiency of DCSBD two channel digital oscilloscope with a bandwidth of 100 MHz, maximal memory depth of 14 Mpts and sampling rate up to 2 GSa/s (Rigol DS2102) was used. For high voltage measurements the Tektronix P6015A

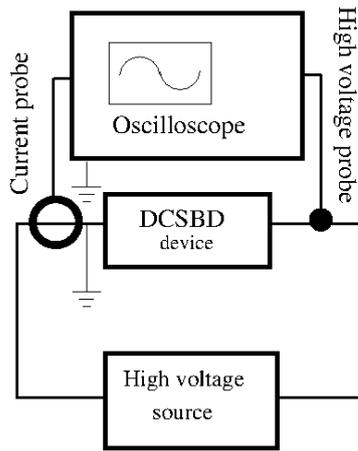


FIGURE 3. Experimental set-up of DCSBD power efficiency measurement.

probe with 1:1000 ratio was used. The current probe Pearson current monitor 4100 with 1:1 ampere-to-volt ratio was used for current measurements. The simple scheme of experimental setup is given in Fig. 3.

3. RESULTS AND DISCUSSION

In this work the energy efficiency of a DCSBD device was calculated as the quotient of the measured high voltage power supply input power and the power-to-plasma estimated from high voltage and current measurements using direct power integration. A typical example of the current and voltage waveforms used for power-to-plasma estimation is given in Fig. 4. In Fig. 4 the capacitive and discharge currents (blue) can be distinguished.

The results of energy efficiency calculations are presented in Fig. 5. This calculation of power-to-plasma was performed using custom-made python or MATLAB script. The corresponding error of estimation, estimated from law of the error propagation, was determined to be approx. 6%.

In the process of DCSBD power-to-plasma estimation from current-voltage measurements, several sources of errors could be identified when the direct integration method is used. In this paper, three sources of errors are discussed using simulations in the MATLAB computational environment on the real measured data of a DCSBD discharge operated at an input power of 250 W (measured input power of the power supply unit).

The first source of errors was identified in the process of finding the zero-phase, i.e., the beginning of the discharge periods. The zero-phase of input voltage defines the interval of integration, so a false estimate could introduce a systematic power estimation error. In Fig. 6 the effect of zero-phase misalignment is shown on the y-axis. It can be seen that even 500 ns of zero-phase point misalignment does not induce significant errors of power-to-plasma estimation. This can be explained taking into account that the active

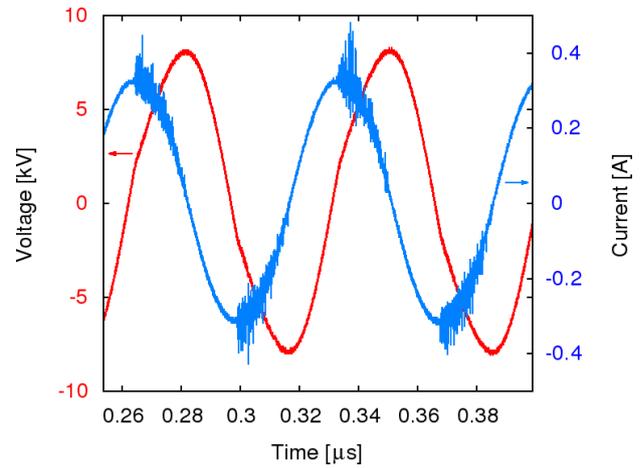


FIGURE 4. Typical current and voltage waveforms of DCSBD operated at input power of approx. 250 W.

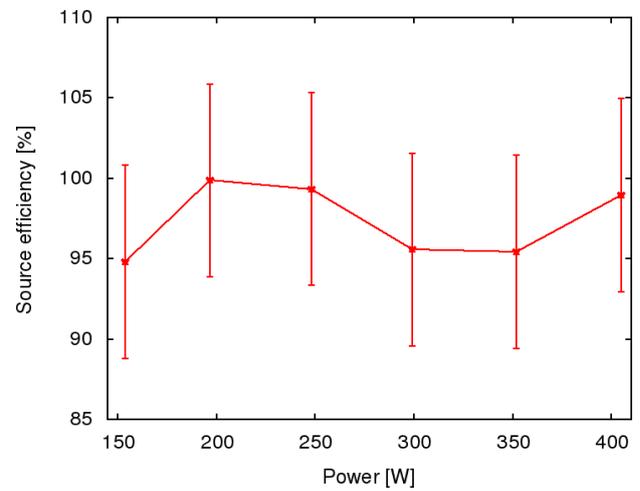


FIGURE 5. Energy efficiency of the DCSBD device for selected input power of the power supply unit.

"discharge" phase starts at about the $\pi/4$ phase of the input high voltage.

The second source of errors was identified in the current-voltage phase shift of the recorded waveforms. This phase shift could easily result from unequal length of the probe-to-scope cables, or from inaccurate oscilloscope parameters. Fig. 6 shows the effect of a current-voltage phase shift. It can be seen that this phase shift has a measurable effect on the estimated power-to-plasma value. A phase shift on the scale of several tens of ns shifts the estimated power input value by up to several tens of Watts. This effect can be explained by the capacitive coupling of the DCSBD. An ideal capacitor introduces a current-voltage phase shift of $\pi/2$, so the real power over one period is zero. When the phase shift of current-voltage waveforms is introduced, a phase shift other than $\pi/2$ occurs and the net integrated power consumption becomes non-zero.

A third source of errors was identified at the internal A/D converter of the oscilloscope – the precision, or the noise level of the sampled signal. This level

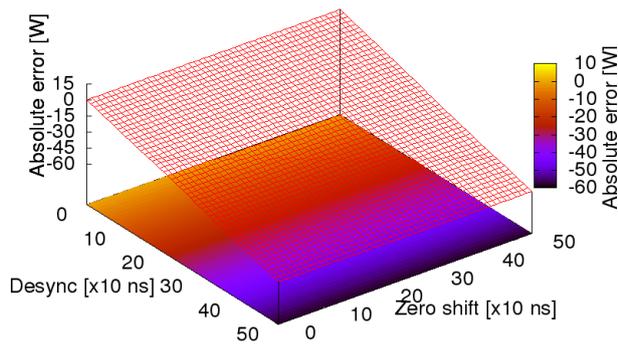


FIGURE 6. Absolute error of power-to-plasma/discharge power estimation using direct numerical integration of discharge current-voltage waveforms. The error of estimation resulting from a) a bad estimate of the beginning of the discharge period (labeled as 'zero shift' misalignment) and b) voltage-to-current waveform phase misalignment (i.e., voltage-current phase shift, labeled as 'desync') are given. The zero reference point (the origin) is given as the power estimated from raw measured data.

of conversion noise is inherent in each oscilloscope and cannot be lowered easily. In Fig. 7 the real current-voltage signal from the oscilloscope was salted with uniformly distributed pseudo-random noise of the magnitude of N bits of A/D converter resolution and then the input power was estimated. The average of 10 salted signal power estimates is used as a point in the graph. It can be seen that when high added noise is present the absolute error of the power input estimation can be more than 0 to 12 W at 250 W of input power (i.e., 5% of the value).

From the results we can conclude that the power-to-plasma numeric estimation procedure is relatively robust. The artificially introduced current-voltage waveform distortions lead to relatively small errors in power estimation of the order of a few percent, even when relatively bad experimental conditions are assumed (i.e., phase shift < 50 ns and noise level < 2 bits). This error magnitude is comparable or less than the assumed measurement errors of current and voltage waveforms.

4. CONCLUSIONS

In the first part of the experiments presented here, the energy efficiency of the DCSBD device was calculated as the ratio of power-to-plasma and the input power of the power supply unit. Results have been given for DCSBD operated at a frequency of about 15 kHz and input power ranging from 100 to 450 W. We found that the DCSBD device has energy efficiency higher than 85% in the given power input range. In the second part of our study, a power input of 250 W was chosen and the estimation of errors for power-to-

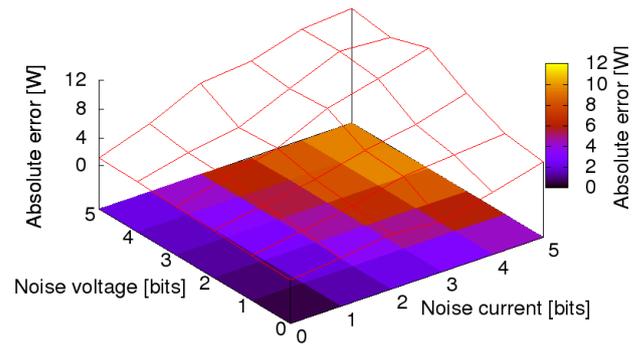


FIGURE 7. Absolute error of power-to-plasma/discharge power estimation using direct numerical integration of discharge current-voltage waveforms. The error of estimation due to the artificial noise (salt) added to the current and/or the voltage waveforms are visualized. The zero reference point (origin) is given as the power estimated from the raw measured data.

plasma were investigated using numerical simulations. The MATLAB computational environment was used to estimate the importance of making a precise calculation of the beginning of the discharge periods (zero-phase estimation), the influence of the phase shift of the voltage and current measurements, and the sensitivity of the process to the pseudorandom noise added to the real measured current and voltage waveforms. We have found that the power estimation process is relatively robust and stable, as the artificially induced power estimation errors were less than or equal to the assumed measurement errors, i.e., they were of the order of a few percent under a phase shift of < 50 ns and an added noise level of < 2 bits. The influence of zero-phase estimation was found to be negligible, provided that the phase misplacement is well below $\pi/4$.

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